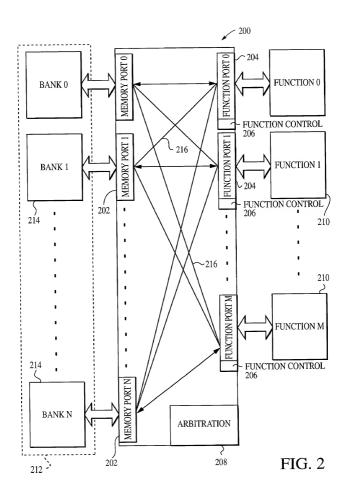
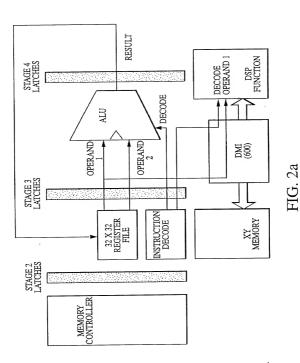


FIG. 1





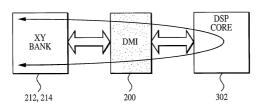


FIG. 3

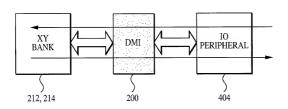
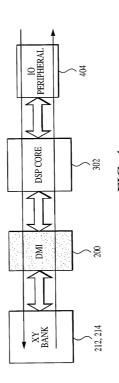


FIG. 5



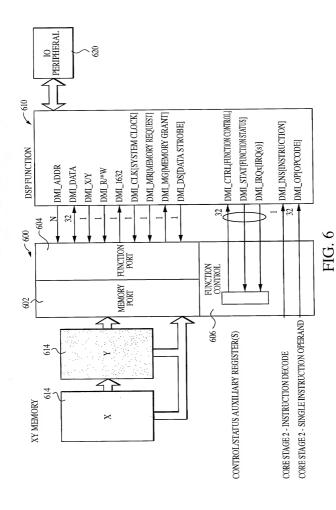
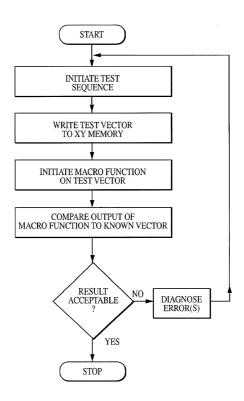
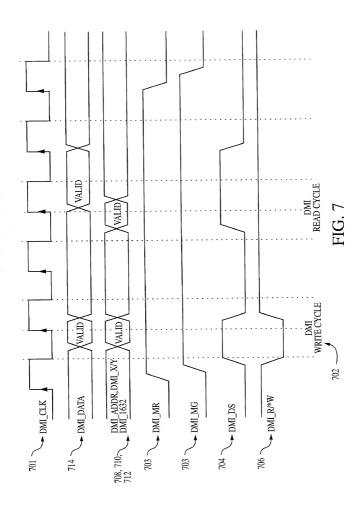
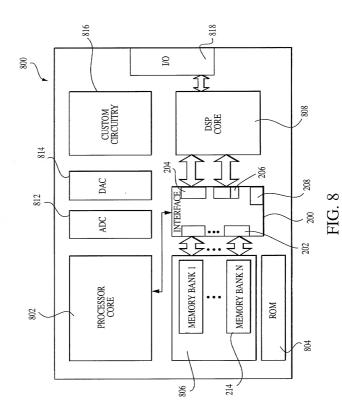


FIG. 6a







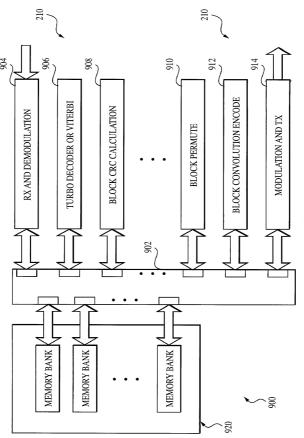


FIG. 9

	Data Block 0	Data Block 1	Data Block 2	Data Block 3	Data Block 4	:
Bank 0	Function 0	Function 1	Function 2	Function 3	Function 0	
Bank 1	-	Function 0	Function 1	Function 2	Function 3	
Bank 2	-		Function 0	Function 1	Function 2	
Bank 3		•	1	Function 0	Function 1	

FIG. 9a

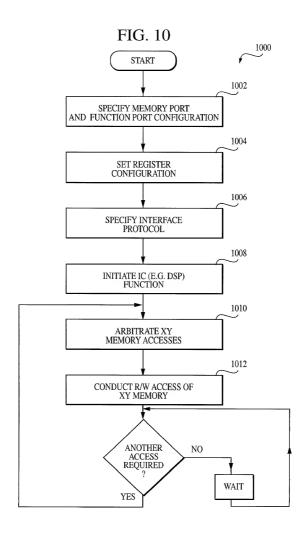


FIG. 11

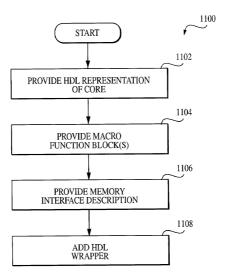


FIG. 11a 1120 SPECIFY MEMORY INTERFACE REQUIREMENTS 1122 1108 CREATE DIRECTORY AND COPY FILES 1124 GENERATE STRUCTURAL HDL 1126 GENERATE (i) SIMULATION MAKE FILE (ii) SYNTHESIS SCRIPT SIMULATE OR SYNTHESIZE 1128 1130 SIMULATE SYNTHESIZE DESIGN DESIGN

